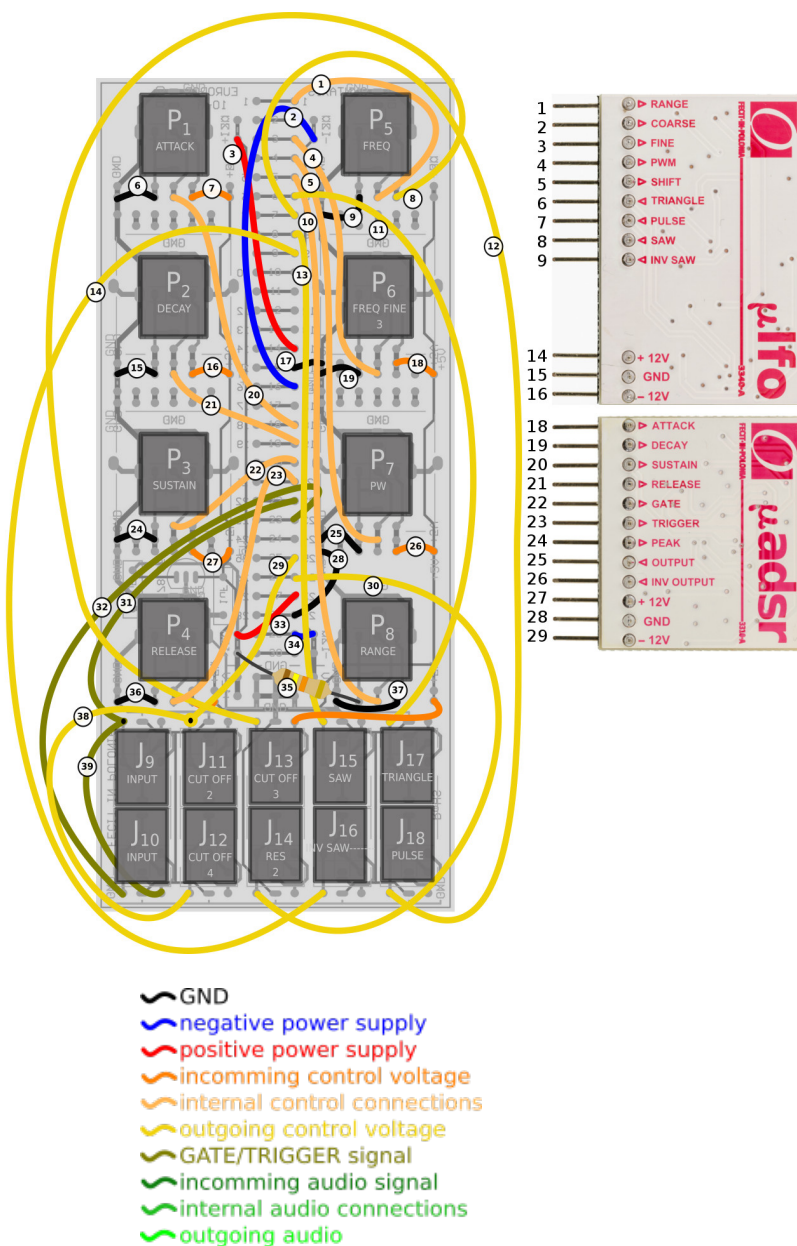


ENV/LFO

1.1

integrated modulation source



Features

- two independent modulation sources: ADSR envelope and LFO
- ADSR envelope with 5Vpp range
- RC envelope shape
- independent gate and trigger
- positive and negative envelope output
- 4 independently accessible LFO waveforms: triangle, saw, inverted saw, pulse
- 5Vpp LFO output signal with adjustable DC shift
- pulse width control for the pulse wave
- coarse and fine frequency control
- all outputs buffered and protected
- power supply -12V, GND, +12V
- on-board +5V regulator
- EURORACK compatible power supply connector 2x5 pins
- EURORACK dimensions: 10HP module width, 38mm module depth

Components

EURORACK-10-A PCB	1
μ LFO-3340-A	1
μ ADSR-3310-A	1
potentiometer 9mm Alpha, T18 shaft	8
jack socket Thonkiconn	10
79L05 regulator	1
1 μ F capacitor (PP or PA)	2
pin headers 2x5	1

Controls

ADSR

P1	ATTACK manual control
P2	DECAY manual control
P3	SUSTAIN manual control
P4	RELEASE manual control
J9	GATE input
J10	TRIGGER input; defaults to GATE when J10 not plugged
J11, J12	envelope output
J14	inverted envelope output

LFO

P5	LFO frequency manual control — coarse
P6	LFO frequency manual control — fine
P7	puls width manual control for LFO pulse wave
P8	range selector; CCW position: $-5V \div 0V$, center: $-2.5V \div +2.5V$; CW: $0V \div +5V$
J13	additional frequency voltage control; summed with P5 manual setting
J15	saw output
J16	inverted saw output
J17	triangle output
J18	pulse output

ad P8. The potentiometer provides control voltage for the SHIFT input of the μ LFO. The voltage should be in the range from +10V in CCW position to 0V in CW position. This results in DC shift of the LFO output wave, as indicated above. The resistor value should be equal to $\frac{1}{5}$ of the pot value (2k for 10k pot, 20k for 100k pot).

CAUTION: The GATE and TRIGGER inputs of the μ ADSR-3010-A are not protected. Connecting negative voltage to those inputs may damage the μ module.

Potentiometers

- P1 – P8 taper: B
- value: 10k \div 100k

Potentiometer values

	pros	cons
10k Ω	<ul style="list-style-type: none">• lower sensitivity to disturbances• output voltage characteristic closer to linear	<ul style="list-style-type: none">• higher current consumption (0.5mA per pot)
100k Ω	<ul style="list-style-type: none">• lower current consumption (0.05mA per pot)	<ul style="list-style-type: none">• higher sensitivity to disturbances• output voltage curve deformation (when connected to 0 and 5V, 2.5V is further from central pot position)

Adjusting potentiometer range

A potentiometer range may be adjusted with simple modification of the circuit.

If you do not need very small values of some parameter, the lower extreme of the CV range may be cut out by inserting a resistor between the left pad of the potentiometer controlling this parameter and the GND. E.g. in case of B10k Ω pot, inserting a 5k Ω resistor will cut $\frac{1}{3}$ of the CV range out, the minimal control voltage will be ca. 1.7V.

Similarly, the upper extreme of the CV range may be cut out by inserting a resistor between the right potentiometer pad and the +5V voltage.

+5V regulator

Please, refer to the EUROPCB-10-A datasheet.

0 \div +10 CV range

Control inputs may be adapted to accept voltage range 0V \div +10V by connecting control voltage to a μ module through 100k Ω resistor. This works for all control inputs. Also, the 78L05 regulator should be replaced with 78L10.

Assembly hints

1. Solder the wire connections before, then the components (pots, regulator, μ modules, etc.).
2. Make short connections before the long ones.
3. Keep the connection wires loose enough to be able to structure them after mounting the components.
4. When making connections leave enough space for the components and for accessing their solder points.

5. Solder the components from the smallest to the biggest:

- the +5V regulator and the capacitors,
- the pin header 2x5,
- the Thonkiconn jack sockets,
- the 9mm potentiometers,
- the μ modules.

Connections (checklist)

1	P5 ► CENTER	↔	μ LFO ► RANGE	
2	-12V	↔	μ LFO ► -12V	
3	+12V	↔	μ LFO ► +12V	
4	P6 ► CENTER	↔	μ LFO ► FINE	
5	P7 ► CENTER	↔	μ LFO ► PWM	
6	GND	↔	P1 ► LEFT	
7	+5V	↔	P1 ► RIGHT	
8	J13 ► LEFT	↔	P5 ► RIGHT	
9	GND	↔	P5 ► LEFT	
10	P8 ► CENTER	↔	μ LFO ► SHIFT	
11	μ LFO ► TRIANGLE	↔	J17 ► LEFT	
12	μ LFO ► PULSE	↔	J18 ► LEFT	
13	μ LFO ► SAW	↔	J15 ► LEFT	
14	μ LFO ► INV SAW	↔	J16 ► LEFT	
15	GND	↔	P2 ► LEFT	
16	+5V	↔	P2 ► RIGHT	
17	GND	↔	μ LFO ► GND	
18	+5V	↔	P6 ► RIGHT	
19	GND	↔	P6 ► LEFT	
20	P1 ► CENTER	↔	μ ADSR ► ATTACK	
21	P2 ► CENTER	↔	μ ADSR ► DECAY	
22	P3 ► CENTER	↔	μ ADSR ► SUSTAIN	
23	P4 ► CENTER	↔	μ ADSR ► RELEASE	
24	GND	↔	P3 ► LEFT	
25	GND	↔	P7 ► LEFT	
26	+5V	↔	P7 ► RIGHT	
27	+5V	↔	P3 ► RIGHT	
28	GND	↔	μ ADSR ► GND	
29	μ ADSR ► OUTPUT	↔	J11 ► LEFT	
30	μ ADSR ► INV OUTPUT	↔	J14 ► LEFT	
31	μ ADSR ► GATE	↔	J9 ► LEFT	
32	μ ADSR ► TRIGGER	↔	J10 ► LEFT	
33	+12V	↔	μ ADSR ► +12V	
34	-12V	↔	μ ADSR ► -12V	
35	+12V	↔	P8 ► LEFT	through resistor
36	GND	↔	P4 ► LEFT	
37	GND	↔	P8 ► RIGHT	
38	J11 ► LEFT	↔	J12 ► LEFT	
39	J9 ► LEFT	↔	J10 ► RIGHT	TRIGGER defaults to GATE